

# Performance Evaluation for Super-Orthogonal Concatenated Convolutional Coding Scheme for Multiple Access Applications over Nonlinear Satellite Channels

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**Abstract** — In digital satellite communications there are many earth stations and satellites sharing the transmission channel hence, a multiple access scheme is needed in order to increase the number of simultaneous users and make efficient utilization of power and bandwidth of the channel. Code-division multiple-access (CDMA) has been considered as one of the most efficient and reliable schemes for multiple access satellite communications. In CDMA system, ultimate capacity can be achieved using low rate error correcting codes to perform both coding and spreading. Orthogonal and super-orthogonal convolutional codes have been proposed for this purpose. The super-orthogonal convolutional code performs well in very low rates and requires less bandwidth expansion than the orthogonal convolutional code. Concatenating two or more super-orthogonal convolutional codes through interleavers achieves near Shannon limit performance. A major drawback in satellite communications is the presence of high power amplifier which is needed to provide the signal power required to overcome the large distance between transmitter and receiver. The high power amplifier introduces nonlinear distortion to the amplified signal when it operates in its maximum power efficiency region which results in degradation to the CDMA signal. In order to compensate for this nonlinear response a linearization technique is employed or the operating point of the amplifier is backed-off to its linear region. In this paper we present a new low rate concatenated convolutional coding scheme suitable for spread spectrum CDMA applications. Analysis for the bit error probability performance of the proposed coding scheme is derived in both additive white Gaussian noise (AWGN) channel, and nonlinear satellite channel. Results showed that, the proposed coding scheme achieves superior performance compared to the conventional low rate convolutional codes.

**Keywords** — CDMA, Code Concatenation, HPA, Low Rate Codes.

## I. INTRODUCTION

Code-division multiple-access [1]-[3] is a widely employed multiple access technique in satellite systems. It has robustness in multipath fading channels and in addition, it has the ability to accommodate large number of users with lower interference levels.

In conventional CDMA system, the presence of the multiple-access interference (MAI) limits the number of simultaneous users supported by the channel. To minimize the effect of MAI, powerful error correcting codes are employed followed by the spreading process which is accomplished using pseudo random sequences [4][5]. It was proved by Viterbi [6][7] that, spread spectrum CDMA systems can reach their maximum theoretical performance

by employing low-rate error correcting codes alone to perform both coding and bandwidth spreading which is known as code-spread. These low rate codes are suitable only for spread spectrum applications where the code rate can be as small as the inverse of the spreading factor of the spreading process.

The early introduced low rate error correcting codes are orthogonal [6], and super-orthogonal convolutional codes [8][9]. Later, super-orthogonal turbo codes [10] was presented which is a concatenation of two or more super-orthogonal convolutional codes. Super-orthogonal turbo code has superior performance to both orthogonal and super-orthogonal convolutional codes. A significant amount of work has been done in order to obtain good low rate codes for application in CDMA systems [11]-[15].

Powerful error correcting codes can be obtained by concatenating at least two codes through interleavers which is known as code concatenation [16]. Two concatenated coding schemes are commonly used: serial concatenation [17], and parallel concatenation [18] [19].

In satellite communications, in order to provide the transmit signal levels needed to overcome the loss between the transmitter and receiver, an efficient high power amplifier (HPA) is employed. Power amplifier operates most efficiently at its saturation region. As driven near saturation, the power amplifier exhibits nonlinear distortion [20],[21] which causes degradation to the signal-to-noise ratio (SNR), and interference to adjacent channels.

In this study, a new low rate concatenated convolutional coding scheme is presented called super-orthogonal parallel-serial concatenated convolutional code (SOPSCCC). The proposed concatenated coding scheme is suitable for spread spectrum applications. The bit error probability performance of the coding scheme is studied in both AWGN channel, and nonlinear satellite channel where a nonlinear high power amplifier is employed. Results proved the superior performance of the proposed concatenated coding scheme to that of the classical low rate concatenated convolutional codes.

This paper is organized as follows, in section II a brief review on orthogonal and super-orthogonal convolutional codes is presented. Section III presents an overview for the conventional concatenated convolutional coding schemes. In section IV, a review on high power amplifier is introduced. Section V introduces the construction of the proposed SOPSCCC. Analysis of the SOPSCCC is illustrated in section VI and the results for the bit error probability performance is presented in section VII. In section VIII conclusion is discussed.

## II. ORTHOGONAL AND SUPER-ORTHOGONAL CONVOLUTIONAL CODE

Orthogonal convolutional codes are low rate codes whose codewords are orthogonal to each other. Encoder structure for an orthogonal convolutional code is shown in Fig. 1. The encoder consists of a  $k$ -stage shift register, the states of the shift register are connected to an orthogonal signal selector. One of the possible ways to obtain orthogonal signal set is by using Hadamard matrices [10].

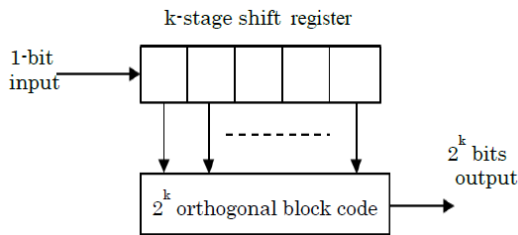


Fig.1. Block diagram for an orthogonal convolutional encoder.

Every one bit input, all the  $k$  bits (contents of the shift register) are used to select a row from the orthogonal code. The selected codewords are orthogonal to each other since they are generated from an orthogonal signal set. Every bit interval the encoder outputs one of  $2^k$  codewords each of length  $2^k$  bits so, the code rate of the orthogonal encoder is  $1/2^k$ . By performing some modification on the orthogonal convolutional encoder, a new class of orthogonal convolutional codes is obtained called, super-orthogonal convolutional codes. Encoder structure of the super-orthogonal convolutional code is the same as that of orthogonal convolutional code except that, only the inner  $k-2$  stages of the shift register are used to select a codeword from the orthogonal code hence, the code rate of the super-orthogonal convolutional code is  $1/2^{k-2}$ .

## III. CODE CONCATENATION

### A. Serial concatenated convolutional code

Serial concatenated convolutional code, as shown in Fig. 2, is derived by combining an outer code  $C_o$ , and an inner code  $C_i$ , through an interleaver with length  $N$ . The outer code has rate  $R_o = h/p$  and the inner code has rate  $R_i = p/n$ , the resulting SCCC,  $C_s$  has rate  $R_s = h/n$ .

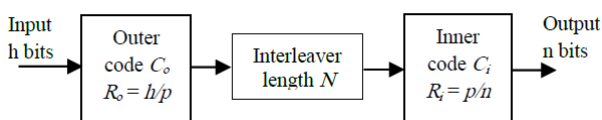


Fig.2. Serial concatenated convolutional code.

For a serial concatenated convolutional code, the conditional weight enumerating function (CWEF),  $A^{C_s}(w, X)$  which enumerates the weights of all codewords associated with particular information weights is expressed as [17]

$$A^{C_p}(w, X) = \sum_{z=0}^N \frac{A_{w,z}^{C_o} \times A^{C_i}(z, X)}{\binom{N}{z}} \quad (1)$$

where  $A_{w,z}^{C_o}$  is the number of codewords of the outer code,  $C_o$  that have weight  $z$  associated with an information word of weight  $w$ , and  $A^{C_i}(z, X)$  is the CWEF of the inner code  $C_i$ .

### B. Parallel concatenated convolutional code

Parallel concatenated convolutional code (PCCC), that is shown in Fig. 3, consists of two codes,  $C_1$  with rate  $R_1 = h/n_1$ , and  $C_2$  with rate  $R_2 = h/n_2$  joined by an interleaver with length  $N$ . The output code word length  $n = n_1 + n_2$  and the overall PCCC rate is  $R_p = h/n$ .

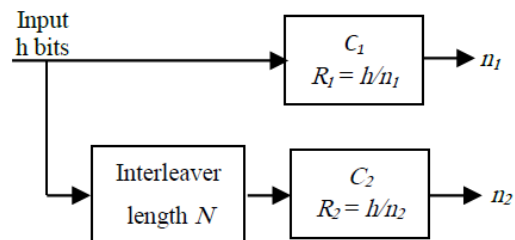


Fig.3. Parallel concatenated convolutional code.

CWEF of a parallel concatenated convolutional code is expressed as [18][19]

$$A^{C_p}(w, X) = \frac{A^{C_1}(w, X) \times A^{C_2}(w, X)}{\binom{N}{w}} \quad (2)$$

where  $A^{C_1}(w, X)$  and  $A^{C_2}(w, X)$  are the CWEFs of the two constituent codes  $C_1$  and  $C_2$  respectively.

## IV. HIGH POWER AMPLIFIER

In satellite communication systems, because the large distances between transmitter and receiver, high efficiency power amplifiers are needed. There are two major types of power amplifiers, travelling wave tube amplifiers (TWTAs) and solid state power amplifiers (SSPA). At high power levels, TWTAs offer the best performance in terms of size, cost and efficiency, but lag behind SSPAs in linearity. The non-linear behavior of TWTA is usually described according to Saleh model [22] using amplitude/amplitude (AM/AM), and amplitude/phase (AM/PM) conversions that are illustrated in Fig. 4. One method to avoid the nonlinear effect of the HPA is to utilize a linearization technique which generates a nonlinear inverse function to that introduced by the HPA. The most widely used linearization technique is the predistortion linearization [23]-[24]. Another simple method to compensate for the amplifier nonlinearity is to back-off the operating point of the HPA from saturation to the linear region. The output back-off (OBO) of the HPA is defined as [25]

$$OBO = 10 \log \frac{P_{sat}}{P_{av}} \quad dB \quad (3)$$

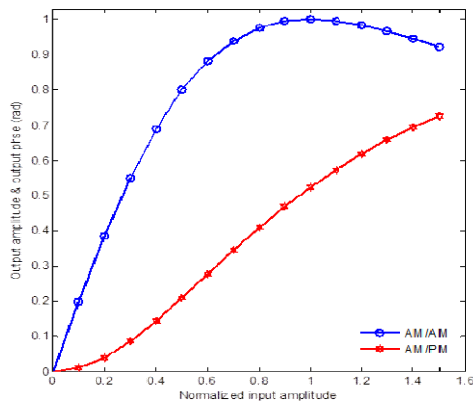


Fig.4. AM/AM and AM/PM conversion curves for travelling wave tube amplifier.

where  $P_{sat}$  is the saturation power of the HPA, and  $P_{av}$  is the average output power of the transmitted signal. For large back-off values, the nonlinear distortion of the HPA can be greatly reduced but, on the other hand, the power efficiency of the amplifier decreases, which leads to an unacceptable performance. So, a trade-off must be made between minimizing the nonlinear effects and maximizing the power efficiency of the HPA.

### V. CONSTRUCTION OF THE PROPOSED LOW RATE CONCATENATED CODING SCHEME

The super-orthogonal parallel-serial concatenated coding scheme presented in this study combines the principles of low rate coding with that of both parallel and serial concatenation. Block diagram of the coding scheme is illustrated in Fig. 5. It is constructed out of two serially concatenated codes, each of them is a serial concatenation of a classical convolutional code and a super-orthogonal convolutional code. The two serially concatenated codes are then further concatenated in parallel. The two outer codes,  $C_{o1}$  and  $C_{o2}$  of the two serially concatenated codes,  $C_{S1}$  and  $C_{S2}$  are two identical rate 1/1 convolutional codes whose generator matrix is

$$G(D) = [I (I+D^2)/(I+D+D^2)] \quad (4)$$

and whose encoder block diagram is shown in Fig. 6. The rate 1/1 convolutional encoder can be obtained from the rate 1/2 recursive systematic convolutional encoder of Fig. 7 by not sending the systematic bit of the encoder. The two inner codes,  $C_{i1}$  and  $C_{i2}$  are two identical super-orthogonal convolutional codes that are described in section II. In the proposed concatenated coding scheme, we have  $2^{k-2}$  output bits from  $C_{S1}$  and  $2^{k-2}$  output bits from  $C_{S2}$  so, the overall SOPSCCC rate is  $R=1/(2 \times 2^{k-2})$ .

### VI. PERFORMANCE ANALYSIS

The proposed coding scheme is originally a parallel concatenation of two convolutional codes,  $C_{S1}$  and  $C_{S2}$ . For a parallel concatenated convolutional code, the bit error

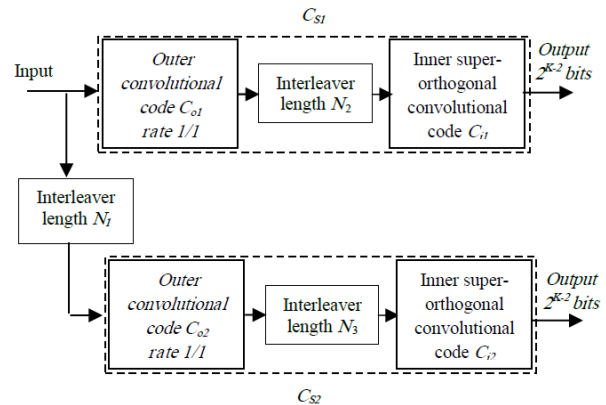


Fig.5. Block diagram of the proposed SOPSCCC.

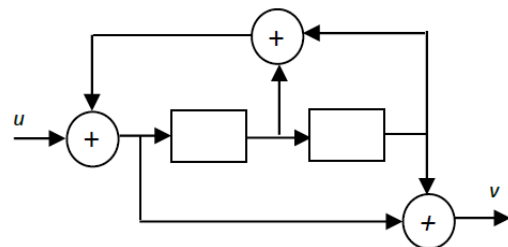


Fig.6. Block diagram of the outer rate 1/1 convolutional Encoder.

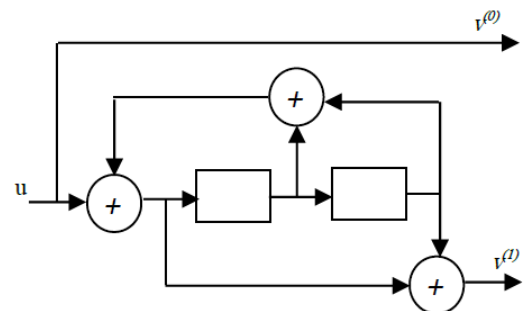


Fig.7. Block diagram of a rate 1/2 recursive systematic convolutional encoder.

probability  $P_b(e)$  under maximum-likelihood decoding for binary phase shift keying modulation in an AWGN channel can be upper-bounded as [18]

$$P_b(e) \leq \sum_{w=1}^N \frac{w}{N} W^w A^{C_p}(w, X) \Big|_{W=X=e^{-R_c E_b / N_0}} \quad (5)$$

where  $A^{C_p}(w, X)$  is the CWF of the parallel concatenated convolutional code,  $R_c$  is the rate of the parallel concatenated code,  $N$  is the interleaver length and  $E_b/N_0$  is the signal-to-noise ratio (SNR) per bit. So, to analyze the performance of the proposed coding scheme, first we have to calculate its CWF which is determined by the CWFs of the constituent codes from which the code is constructed. In the analysis.

#### A. Calculating CWF of the outer code $C_{o1}$

An important tool that is used to calculate the weight enumerating functions of a convolutional encoder is the transfer function of the encoder which is determined using its modified state diagram. Modified state diagram of an encoder is a modified version of its state diagram. It provides a complete description for the Hamming weights

of all nonzero codewords, that is, a codeword *weight enumerating function* (WEF). The modified state diagram of the outer rate 1/1 convolutional encoder of Fig. 6 is shown in Fig. 8.

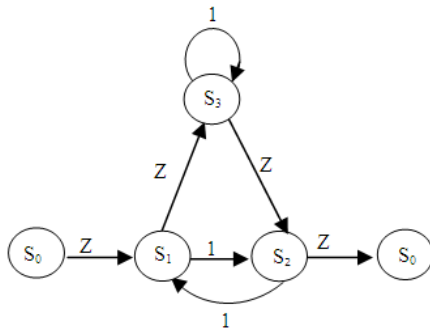


Fig.8. Modified state diagram of the outer rate 1/1 convolutional encoder.

In the modified state diagram of Fig. 8, the power of  $Z$  in a branch is the weight of the  $n$  encoded bits on that branch.

There are two definitions related to the modified state diagram and are used in computing its transfer function. These definitions are:

- **Forward path:** Forward path is the path connecting the initial state to the final state and does not go through any state twice. The *path gain* is the product of the branch gains along the path. We will denote the gain of the  $i^{th}$  forward path as  $F_i$ .
- **Cycle:** A *cycle* is a closed path starting at any state and returning to the same state without going through any other state twice. The gain of the  $i^{th}$  cycle will be denoted as  $C_i$ .

A set of cycles is *non-touching* if no state belongs to more than one cycle in the set. Let  $\{i\}$  be the set of all cycles in the modified state diagram,  $\{i', j'\}$  be the set of all pairs of non-touching cycles,  $\{i'', j'', l''\}$  be the set of all triples of non-touching cycles, and so on. A quantity is then defined as [26]

$$\Delta = 1 - \sum_i C_i + \sum_{i,j} C_i C_j - \sum_{i,j,l} C_i C_j C_l + \dots, \quad (6)$$

where  $\sum_i C_i$  is the sum of the cycle gains,  $\sum_{i,j} C_i C_j$  is the product of the cycle gains of two non-touching cycles summed over all pairs of non-touching cycles,  $\sum_{i,j,l} C_i C_j C_l$  is the product of the cycle gains of three non-touching cycles summed over all triples of non-touching cycles, and so on. Finally,  $\Delta$  is a quantity defined exactly like  $\Delta$  but only for the portion of the graph not touching the  $i^{th}$  forward path. Transfer function,  $A(Z)$  of the modified state diagram can then be calculated as [27]

$$A(Z) = \frac{\sum_i F_i \Delta_i}{\Delta} \quad (7)$$

If the modified state diagram is augmented by labeling each branch corresponding a nonzero information block with  $W^w$ , where the power of  $W$  is the weight of the input information bits in that branch, and labeling each branch with  $L$ , then, the codeword *input-output weight enumerating function* (IOWEF) of the encoder, which is the transfer function of its augmented modified state diagram, is expressed as [26]

$$A(W, Z, L) = \sum_{w,z,l} A_{w,z,l} W^w Z^z L^l \quad (8)$$

where  $A_{w,z,l}$  denotes the number of codewords with weight  $z$ , whose associated information weight is  $w$ , and whose length is  $l$  branches. The augmented modified state diagram for the outer encoder is shown in Fig. 9.

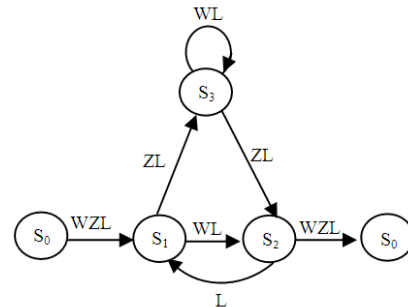


Fig. 9. Augmented modified state diagram of the outer rate 1/1 convolutional encoder.

In the augmented modified state diagram of Fig. 9:

- There are 3 cycles;  
Cycle 1:  $S_1 S_3 S_2 S_1$  ( $C_1 = Z^2 L^3$ )  
Cycle 2:  $S_1 S_2 S_1$  ( $C_2 = WL^2$ )  
Cycle 3:  $S_3 S_3$  ( $C_3 = WL$ )
- There is one pair of non-touching cycles;  
Cycle pair 1: Cycle 2, Cycle 3 ( $C_2 C_3 = W^2 L^3$ )
- There are no other sets of non-touching cycles.  
Therefore, according to (6), can be expressed as  
 $= 1 - (Z^2 L^3 + WL^2 + WL) + (W^2 L^3)$   
 $= 1 - Z^2 L^3 - W(L + L^2) + W^2 L^3$  (9)

After computing  $\Delta_i$  we need to calculate the quantity  $\Delta_i$ . In the augmented modified state diagram of Fig. 9 there are two forward paths, the paths and their gains are:  
Forward path 1:  $S_0 S_1 S_2 S_0$  ( $F_1 = W^3 Z^2 L^3$ )  
Forward path 2:  $S_0 S_1 S_3 S_2 S_0$  ( $F_2 = W^2 Z^4 L^4$ ) (10)  
The subgraph not touching the forward path 1 is the single branch around state  $S_3$  which is shown in Fig. 10.



Fig.10. Subgraph of Fig.9 not touching the forward path 1.

For the sub-graph illustrated in Fig. 10 we can find  $\Delta_1$  as

$$\Delta_1 = 1 - WL \quad (11)$$

Forward path 2 touching all states in the graph so, there are no subgraphs not touching the forward path 2 then,  
 $\Delta_2 = 1$  (12)

Now, we can find  $F_1 \Delta_1$  for the augmented modified state diagram as

$$\begin{aligned} F_1 \Delta_1 &= F_1 \Delta_1 + F_2 \Delta_2 \\ &= W^3 Z^2 L^3 (1 - WL) + W^2 Z^4 L^4 \cdot 1 \\ &= W^2 Z^4 L^4 + W^3 Z^2 L^3 - W^4 Z^2 L^4 \end{aligned} \quad (13)$$

Finally, substituting from (9) and (13) into (7), the IOWEF,  $A(W, Z, L)$  of the outer code can be calculated as

$$A^{C_{o1}}(W, Z, L) = \frac{W^2 Z^4 L^4 + W^3 Z^2 L^3 + W^4 Z^2 L^4}{1 + Z^2 L^3 + W(L + L^2) + W^2 L^3} \quad (14)$$

CWEF of the encoder, which enumerates the weights of all information blocks generating a particular output weight  $z$ , is expressed as [26]

$$A(W, z) = \sum_w A_{w,z} W^w \quad (15)$$

where  $A_{w,z}$  is the number of codewords having output weight  $z$  generated by information words of weight  $w$ .

From (14), and following the procedures illustrated in [26], CWEF,  $A^{C_{o1}}(W, z)$  of the outer constituent code  $C_{o1}$  can be calculated as

$$\begin{aligned} A^{C_{o1}}(W, 2) &= 7W^3 + 5W^4 + 3W^5 + W^6 \\ A^{C_{o1}}(W, 3) &= 0 \\ A^{C_{o1}}(W, 4) &= 6W^2 + 13W^3 + 16W^4 + 10W^5 + 14W^6 + 7W^7 \\ A^{C_{o1}}(W, 5) &= 0 \\ A^{C_{o1}}(W, 6) &= 3W^2 + 7W^3 + 3W^4 + 12W^5 + 12W^6 + W^7 + W^9 \\ A^{C_{o1}}(W, 7) &= 0 \\ A^{C_{o1}}(W, 8) &= 3W^4 + 2W^5 \quad (16) \end{aligned}$$

From the above equation we can calculate the parameters  $A_{w,z}^{C_{o1}}$  which is the number of codewords of the outer code having output weight  $z$  generated by information words of weight  $w$ . For example, there are 7 codewords that have input weight  $w=3$  and output weight  $z=2$ , 5 codewords have input weight  $w=4$  and output weight  $z=2, \dots$  and so on.

#### B. Calculating CWEF of the inner code $C_{i1}$

For a super-orthogonal convolutional code with  $k$ -stage shift register, the input-output weight enumerating function (IOWEF),  $A(Z, X, L)$ , which enumerates the number of codewords with weight  $x$  generated from information word with weight  $z$  and whose length is  $l$  branches, is expressed as [28]

$$A(Z, X, L) = \frac{\nu^{k+2} X L^k}{1 - \nu Z L (1 + \nu L + \dots + \nu^{k-3} L^{k-3}) - \nu^{k-2} Z L^{k-1}} \quad (17)$$

where  $\nu = (X^2)^{k-3}$

In this study we investigated the performance of the SOPSCCC for an inner super-orthogonal convolutional code with memory size  $k=4, 5, 6, 7$ , and 8 which results in an overall SOPSCCC rate  $R=1/8, 1/16, 1/32, 1/64$ , and  $1/128$ . For  $k=4$ , equation (17) can be written as

$$A(Z, X, L) = \frac{X^{12} Z L^4}{1 - X^2 Z L - X^4 Z L^2 - X^4 Z L^3} \quad (18)$$

Using (18) and following the procedures described in [26] we can calculate the CWEF,  $A^{C_{i1}}(z, X)$  of the inner super-orthogonal convolutional code for  $k=4$  which is get as

$$\begin{aligned} A^{C_{i1}}(1, X) &= 7X^{12} \\ A^{C_{i1}}(2, X) &= 6X^{14} + 9X^{16} + X^{24} \\ A^{C_{i1}}(3, X) &= 5X^{16} + 8X^{18} + 4X^{18} + 8X^{20} + 4X^{26} + 2X^{28} \\ A^{C_{i1}}(4, X) &= 4X^{18} + 15X^{20} + 12X^{22} + 6X^{24} \\ A^{C_{i1}}(5, X) &= 12X^{20} + 12X^{22} + 6X^{24} \\ A^{C_{i1}}(6, X) &= 2X^{22} + 10X^{24} \\ A^{C_{i1}}(7, X) &= X^{24} \quad (19) \end{aligned}$$

CWEFs of  $C_{i1}$  for the remaining values of  $k$  can then be calculated. For each of the calculated CWEFs, substituting into (1) by the CWEF of the inner code, and by the

parameters  $A_{w,z}^{C_{o1}}$  calculated from (16) for the outer code  $C_{o1}$ , we can calculate the CWEF,  $A^{C_{s1}}(w, X)$  of the serially concatenated code  $C_{s1}$ .

As the overall proposed coding scheme is a parallel concatenation of the two codes,  $C_{s1}$  and  $C_{s2}$ , the CWEF of the SOPSCCC can be expressed using (2) as

$$A^{SO}(w, X) = \frac{A^{C_{s1}}(w, X) \times A^{C_{s2}}(w, X)}{\binom{N}{w}} \quad (20)$$

Since the two outer codes  $C_{o1}$  and  $C_{o2}$  are identical and the two inner codes  $C_{i1}$  and  $C_{i2}$  are also identical, the two serially concatenated codes  $C_{s1}$  and  $C_{s2}$  are identical and then their CWEFs are the same so, equation (20) can be rewritten as

$$A^{SO}(w, X) = \frac{[A^{C_{s1}}(w, X)]^2}{\binom{N}{w}} \quad (21)$$

Substituting into the above equation by the calculated CWEFs of the code  $C_{s1}$  for each value of  $k$  we can calculate the CWEF of the SOPSCCC at rates  $R=1/8, 1/16, 1/32, 1/64$ , and  $1/128$ .

## VII. PERFORMANCE RESULTS OF THE SOPSCCC

Bit error probability performance of the proposed low rate concatenated coding scheme is evaluated for rates  $R=1/8, 1/16, 1/32, 1/64$ , and  $1/128$  at interleaver lengths  $N_1=N_2=N_3=N=200$  and results are illustrated in Fig.10. From performance figure we can see that increasing the memory size,  $k$  of the inner super-orthogonal convolutional code and hence, decreasing the rate of the SOPSCCC improves the performance of the code. At very low rates, the performance improvement is slightly increases for further decreasing in rate. On the other hand, increasing the memory size of the encoder results in increased decoding complexity. So, trade-off must be made between improving the performance and decreasing the complexity of the system.

In Fig. 11, the performance of the proposed code is compared with both conventional super-orthogonal turbo code (SOTC) [10], and repeat punctured super-orthogonal turbo code (RPSOTC) [14] at interleaver length  $N=200$  and rate  $R=1/15$ . From results we can see that, the proposed coding scheme has superior performance than both codes. Also, performance of the concatenated coding scheme is studied in the presence of nonlinear high power amplifier, results for the bit error probability performance at OBO=5 and OBO=7 are illustrated in Fig. 12. From results we can see that, increasing the OBO value enhances the bit error probability performance of the system as it minimizes the nonlinear effects of the HPA. On the other hand, increasing the OBO values decreases the power efficiency of the HPA so, again, trade-off is taken between improving the system performance and increasing the power efficiency of the HPA.

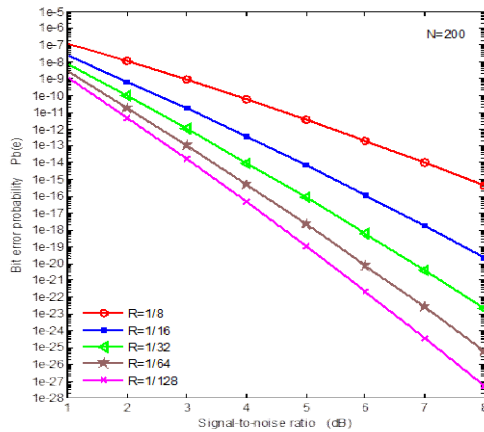


Fig.10. Bit error probability performance of the proposed SOPSCCC in an AWGN channel for interleaver length  $N=200$ .

### VIII. CONCLUSIONS

A low rate concatenated convolutional coding scheme suitable for spread spectrum multiple access applications is investigated. The coding scheme is a parallel-serial concatenation of conventional convolutional codes and super-orthogonal convolutional codes. Performance of the new coding scheme is studied in AWGN channel and results proved that, the proposed coding scheme has better performance than that of the existing super-orthogonal concatenated convolutional codes specially at high SNRs. Performance of the proposed concatenated coding scheme is also studied in the presence of nonlinear high power amplifier which is an essential component in satellite communications. The performance is studied for two OBO values 5 and 7. Smaller OBO values results in larger output power which means higher power efficiency of the amplifier but, on the other hand, this leads to more signal

distortion resulting in more required signal-to-noise ratio for a given bit error rate.

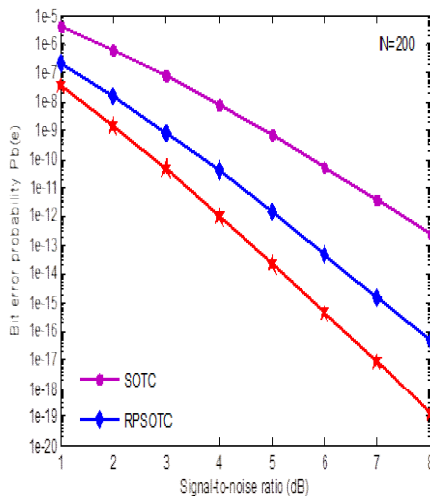


Fig.11. Comparison of the bit error probability performance of SOPSCCC, SOTC, and RPSOTC for rate  $R=1/15$  and  $N=200$ .

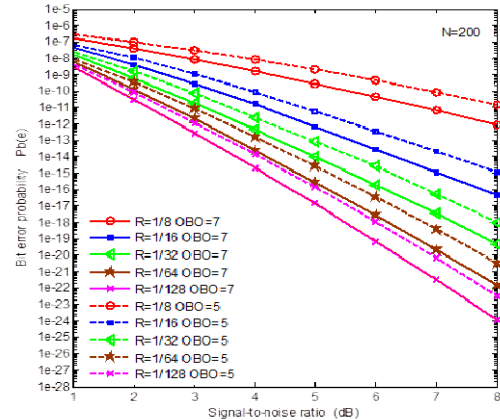


Fig.12. Performance of SOPSCCC at  $OBO=5$ , and  $7$  for interleaver length  $N=200$ .

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